On the left side you find a realization of a XOR gate.

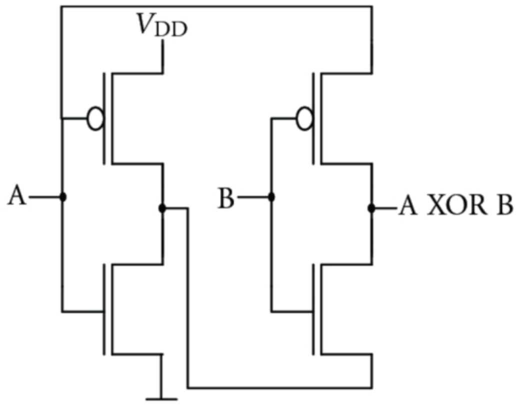
You have to design a hardware Trojan hidden in the XOR gates.

What you can do is to change the characteristics of the transistors:

- permanently connect (disregard the steering signal)
- permanently disconnect (disregard the steering signal)
- change PMOS to NMOS or vice versa

(assumption: if in a certains configuration of the input bits neither VDD nor Drain (ground) is connected to the output wire, then assume that the voltage there stabilizes at random.

Apply the manipulated gates for corrupting CTR_DRBG random numer generator.



Find any solution. It is an opportunity for your creativity!
Have fun!